9.1 Alternative Architectures

- Reduced Instruction Set Computer (RISC) machines that exploit instruction-level parallelism
- Multiprocessing architectures
- Parallel processing
- Explicitly Parallel Instruction Computers (**EPIC**)
- Complex Instruction Set Computers (**CISC**)
- Very Long Instruction Word (**VLIW**) Computers.
- Systolic Arrays
- Dataflow computing
- Neural networks
- Quantum computing