

Figure 3.40 (b) Block Diagram for a 4-Bit Register

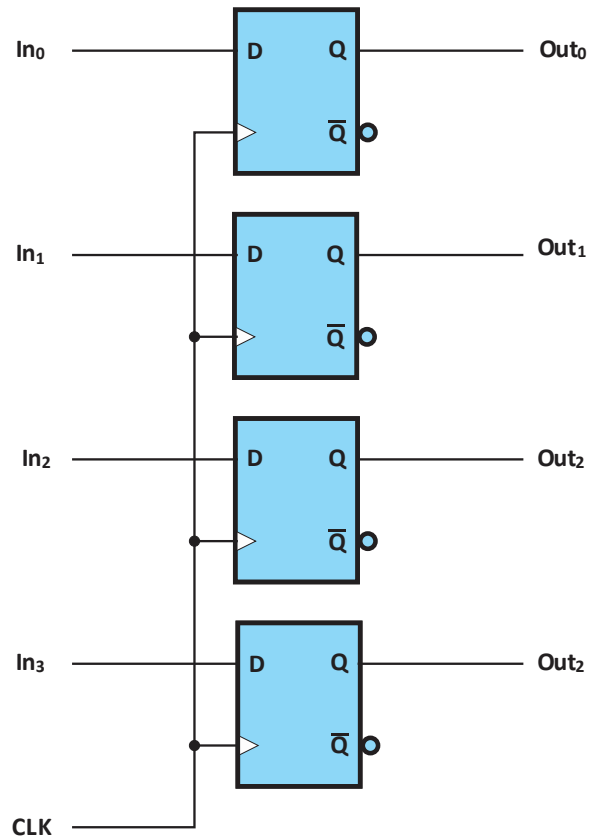


Figure 3.40 (a) Logic Diagram for a 4-Bit Register