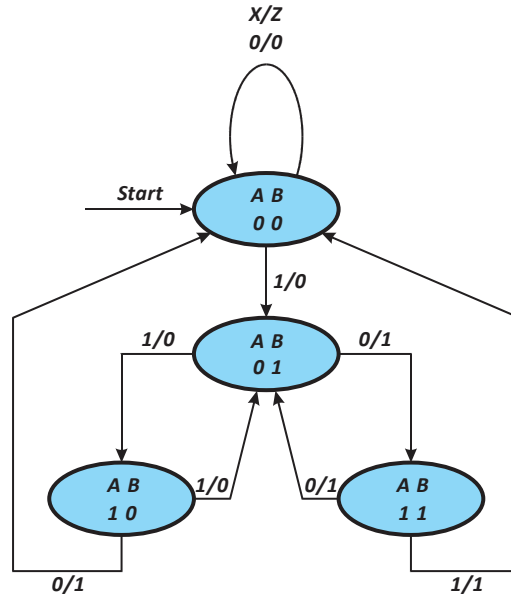


Recall the state diagram of our example.



Arbitrary Synchronous Sequential Circuit State Diagram

Represent the state diagram as a Present-State-Next-State Table

Present State			Input	Next State			Output
	A	B	X		A	B	Z
0	0	0	0	0	0	0	0
	0	0	1	1	0	1	0
1	0	1	0	3	1	1	1
	0	1	1	2	1	0	0
2	1	0	0	0	0	0	1
	1	0	1	1	0	1	0
3	1	1	0	2	1	0	1
	1	1	1	0	0	0	1

Present-State-Next-State Table for the State
Diagram Shown Above