

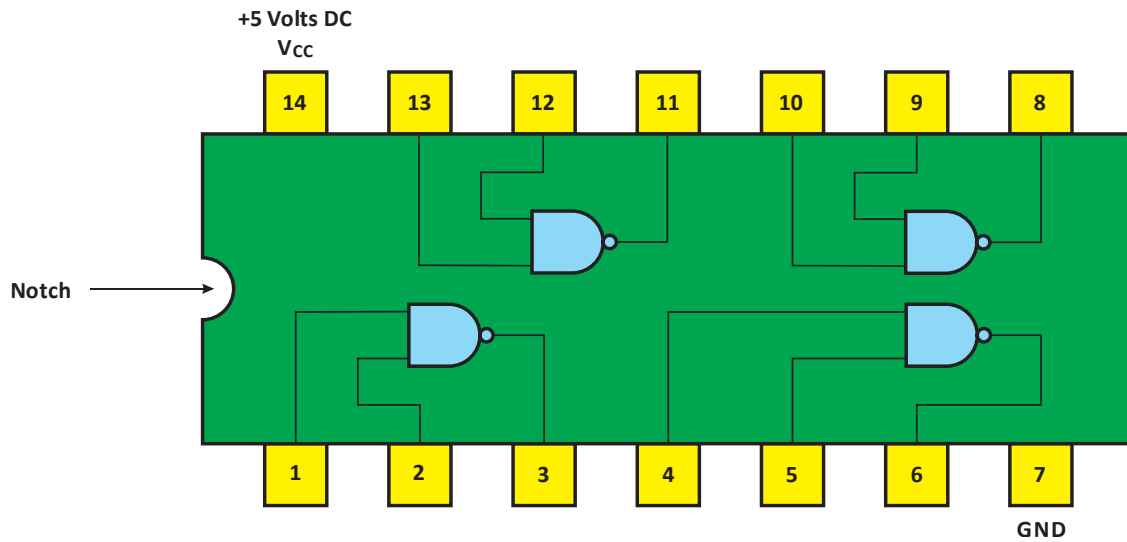
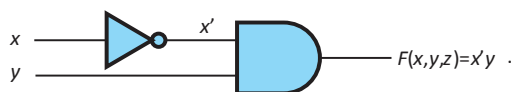


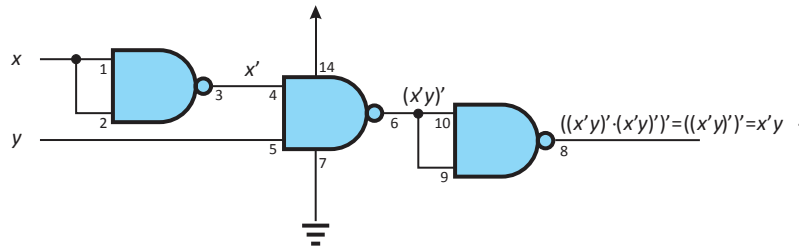
FIGURE 3.19 Texas Instrument SN74LS00 Quadrate 2-Input Positive-NAND Gates

m_i	x	y	z	F
m_0	0	0	0	0
m_1	0	0	1	0
m_2	0	1	0	1
m_3	0	1	1	1
m_4	1	0	0	0
m_5	1	0	1	0
m_6	1	1	0	0
m_7	1	1	1	0

		yz			
x		00	01	11	10
	0	0	0	1	1
	1	0	0	0	0

$$F(x, y, z) = x'y$$





SN74LS00 Quadruple 2-Input Positive-NAND Gates

