

Chapter	Title	Overview
1	Introduction	<ul style="list-style-type: none"> <li>• Computer components</li> <li>• Terminology</li> <li>• History</li> <li>• Computer Level Hierarchy</li> <li>• Cloud Computing</li> <li>• The Von Neumann Model</li> <li>• Non-Von Neumann Models</li> <li>• Parallel Computing</li> </ul>
2	Data Representation in Computer Systems	<ul style="list-style-type: none"> <li>• Positional Numbering System</li> <li>• Converting Between Bases <ul style="list-style-type: none"> <li>◦ Converting to Decimal</li> <li>◦ Converting from Decimal</li> </ul> </li> <li>• Signed Integer Representation</li> <li>• Floating-Point Representation</li> <li>• Character Codes</li> <li>• Error Detection and Correction</li> </ul>
3	Boolean Algebra and Digital Logic	<ul style="list-style-type: none"> <li>• Boolean Algebra</li> <li>• Logic Gates</li> <li>• Digital Components</li> <li>• Combinational Circuits</li> <li>• Sequential Circuits</li> </ul>
4	MARIE: An Introduction to a Simple Computer	<ul style="list-style-type: none"> <li>• CPU Basics and Organization</li> <li>• The Bus</li> <li>• Clocks</li> <li>• The Input/Output Subsystem</li> <li>• Memory Organization and Addressing</li> <li>• Interrupts</li> <li>• MARIE</li> <li>• Instruction Processing</li> <li>• Example Programs</li> <li>• Assemblers</li> <li>• Hardwired Versus Microprogrammed Control</li> <li>• Real-World Examples of Computer Architectures</li> </ul>

Term	Definition
<b>computationally infeasible</b>	An algorithm that requires too much time to run on today's systems. An algorithm that has exponential or greater time complexity.
<b>computer organization</b>	The study of how control signals, signaling methods, memory types, and computation are integrated into a computer system.
<b>computer architecture</b>	The study of the structure and behavior of a computer from a programming perspective. Computer architecture includes the specification of instruction sets, data types, registers, I/O mechanisms, and memory.
<b>instruction set architecture (ISA)</b>	The interface between all software that executes on the machine and the hardware that implements the ISA.