Out text is the primary source of information in this course.


We address five chapters

- Chapter 4 – Implementation of MARIE
- Chapter 5 – A Closer Look at Instruction Set Architecture
- Chapter 6 – Memory
- Chapter 7 – Input/Output and Storage Systems
- Chapter 9 – Alternative Architectures

There is one written assignment per chapter.

We will present no lectures on chapter 4 but, nonetheless,

- students are asked to submit a written assignment on chapter 4.
- students will be tested on material in chapter 4
- the MARIE computer in chapter 4 is a significant foundation for the three projects assigned in this course.