Design a 2 MB memory using 128K × 16 RAM chips and a decoder.
(a) How many 128K × 16 RAM chips are required for the 2 MB memory?
(b) Specify the size of the decoder.
(c) How many address lines are required to access the 2 MB memory?
(d) How many of these lines are connected to the address inputs on all of the 128K × 8 RAM chips?
(e) How many lines must be decoded to produce the chip select inputs?
(f) Draw a logic diagram of the memory?