Using two 4-bit registers $R_1$ and $R_2$, and AND gates, OR gates, and inverters, draw one bit slice of the logic diagram that implements all of the following statements:

- $C_0: R_2 \leftarrow 0$
- $C_1: R_2 \leftarrow \overline{R_2}$
- $C_2: R_2 \leftarrow R_1$